TABLE I. DEVICE PARAMETERS 1/

JPL Part No. 12Ø76-	Turns Ratio	Manufacturer	Mfr. Part No.	Device Type	Package Style	Electrical Performance Characteristics	Terminal Connections	Electrical Test Requirement 2/	Input Impedance 4-8 (Ω)	Weight Max. (gms)	Duty Cycle (%)
L15532F-1	1:1.41	Datatronics	N/A	L	Fig. 1 herein	Table II & III	Fig. 2 herein	Table IV	3,ØØØ min	5	1ØØ
L15532F-2	1:2	Datatronics	N/A	L	Fig. 1 herein	Table II & III	Fig. 2 herein	Table IV	3,ØØØ min	5	1ØØ

NOTES:

1/ This drawing, in conjunction with JPL drawing CS515583 and MIL-T-21Ø38, impose all requirements for procurement of these devices.

2/ Screening shall be in accordance with JPL drawing CS515583.

3. This document takes precedence over documents referenced herein.

RELEASED THRU SECTION 356 DATA MANAGEMENT: DATE:

REVISION: C APPROVED BY: DATE:

APPROVED SOURCE(S)

THE ITEM LISTED IN THE APPROVED SOURCE BLOCK AND IDENTIFIED BY VENDOR NAME, ADDRESS, AND PART NUMBER WILL BE EVALUATED AND TESTED BY THE JPL ELECTRONIC PARTS RELIABILITY SECTION OR ITS DELEGATED ALTERNATE BEFORE BEING APPROVED FOR USE. NON-JPL USERS SHALL CHECK WITH THE ELECTRONIC PARTS RELIABILITY SECTION ON THE STATUS OF THE PART'S APPROVAL BEFORE USING.

VENDOR PART NO	VENDOR	JPL PART NO

JET PROPULSION LABORATORY CALIFORNIA INSTITUTE OF TECHNOLOGY

CAGE NO 23835

Procurement specification:
CS515583
Screening specification:
CS515583

TITLE:
TRANSFORMER,
LOW POWER, PULSE, 1:1.41 & 1:2 TURNS RATIOS, CENTER TAP

DETAIL SPECIFICATION

ST 12Ø76

Custodian: Electronic Parts Reliability Section 514

SHEET 1 OF 4

FILE (Sec. 514): F:\USERS\514\SPECS\ACT-DETL\ST12Ø76.C

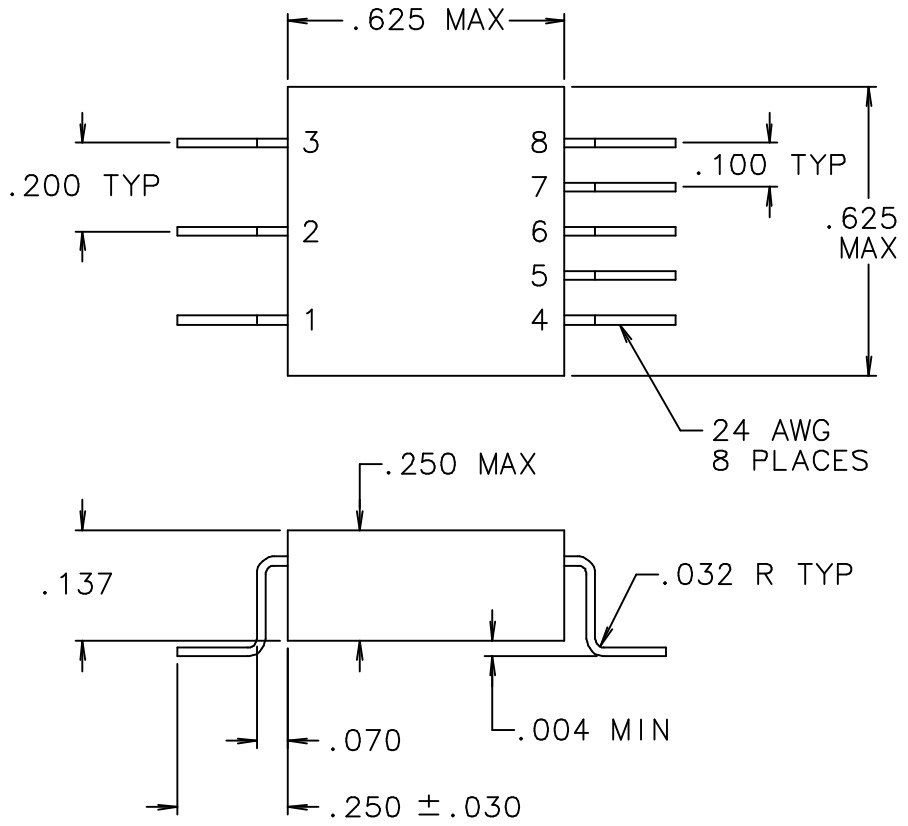

FIGURE 1 Dimensions and Package Style

NOTES:

1. Dimensions are in inches unless otherwise specified. Tolerances: .XXX = ±0.008, .XX = ±0.010
2. Parts shall meet the electrical requirements of Tables I & II herein and shall meet all the requirements of MIL-T-21038.
3. Lead material shall be 24 AWG soft phosphor bronze or solder coated copper and all lead forming shall be completed prior to the initiation of screening.
4. All solder used internal to the device shall be classified as high temperature solder and have a solidus melting point greater than 250°C.

TABLE II. Electrical Performance Characteristics

JPL Part No. 12076 -	Turns Ratio (±3%)	Impedance Min.(KΩ)	Leakage Inductance (µH)	Interwinding Capacitance Max. (pF)	Winding Resistance Max (Ω)		Insulation Resistance Min. (GΩ)	Dielectric Withstanding Voltage(Vrms)	Test Temperature Range(°C)
	1,3:4,8	4-8	1,3:4,8	1,3:4,8	1-3	4-8	1-4	1-4	
L15532F-1	1CT:1.41CT	3.0	1/	50	2.2	2.7	10	100	-55 to +125
L15532F-2	1CT:2CT	3.0	1/	50	2.2	2.7	10	100	-55 to +125

1/ Leakage inductance is a read and record value only and is not a criteria for acceptance or rejection of a device.

TABLE III. Output Waveform Characteristics

JPL Part No. 12076 -	Droop Max. (%) 1/	Overshoot Max. (Vp) 1/	Rise/Fall Time Max. (nS) 1/	Common Mode Rejection Min.(dB) 1/
L15532F-1	20	±1.0	250	45
L15532F-2	20	±1.0	250	45

In accordance with requirements of MIL-STD-1553.

1/ E_{IN} applied @ Wdg. 1-3, 27 V_{P-P}, 250 KHz, TR_{IN} = 90±5 nS.

2/ Waveform parameters shall be measured in accordance with and utilizing the network specified in MIL-T-21038.

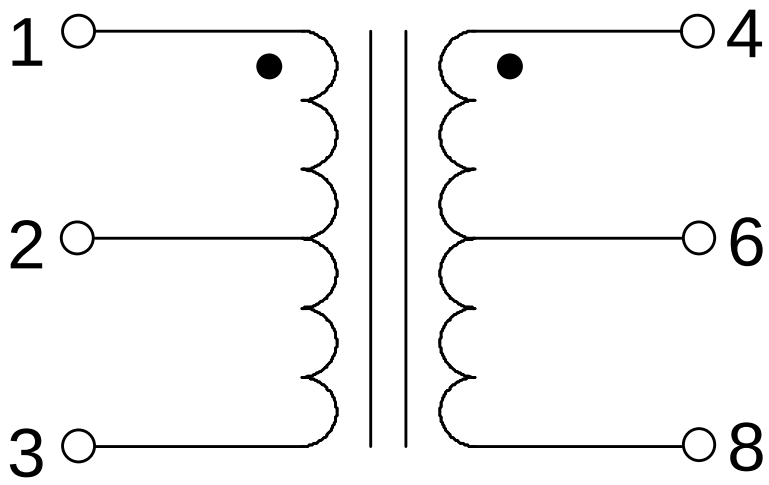

Figure 1. Schematic (Dot denotes like polarities)

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ST 12076	REV C	TITLE: TRANSFORMER LOW POWER, PULSE, 1:1.41 & 1:2 TURNS RATIOS, CENTER TAP	ST	REV
SHEET 4 OF 4			SHEET OF	

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